Serial Number: 09/893,527

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Title: ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

Assignee: Intel Corporation

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IN THE SPECIFICATION

Please amend the specification as follows:

Replace the equation term definitions at page 21, lines 14-18 with the following:

 N_{new} - New number of legs. Device width in schematics before resizing.

 W_{old} - Device width in schematics before resizing.

 $N_{\it old}$ - Old number of legs. Number of legs realizing this device in source layout.

f - Upsize factor

 W_m - Maximal leg size in <u>new</u> layout we wish to occur due to re-legging.